

## **ABSTRACT OF THE DISCLOSURE**

An integrated post-amplifier and laser driver assembly is provided that includes a digital control interface for communication with an external digital IC controller having an I2C bus configured to receive signal parameter programming input from a user. The PA/LD includes one or more DACs which communicate with the digital control interface by way of a glue logic module. A memory is provided in the PA/LD that communicates with the DACs by way of the glue logic module and stores an algorithm for facilitating implementation of changes to signal parameters of one or more signals by way of the DACs. Signal parameter programming instructions are received at the PA/LD from a user by way of the I2C bus, and/or from the internally coded algorithm, and are used by the DACs to facilitate implementation of changes to signal parameters of one or more signals.

WORKMAN NYDEGGER  
A PROFESSIONAL CORPORATION  
ATTORNEYS AT LAW  
1000 EAGLE GATE TOWER  
60 EAST SOUTH TEMPLE  
SALT LAKE CITY, UTAH 84111

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